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This patent resulted from a continuation application of U.S. Patent Application Serial No. 09/228,705, filed January 12, 1999, entitled "Integrated Circuit Device, Synchronous-Link Dynamic Random Access Memory Device", naming Joseph M. Brand as inventor, which is a divisional application of U.S. Patent Number 6,008,074 issued December 28, 1999, entitled "Integrated Circuit Device, Synchronous-Link Dynamic Random Access Memory Device, Method of Forming An Integrated Circuit Device and Method of Forming a Synchronous-Link Dynamic Random Access Memory Edge-Mounted Device", naming Joseph M. Brand as inventor, the disclosure of which is incorporated by reference.

In the Claims

Please cancel claims 1-40 without prejudice. Please add the following new claims 41-61.

NEW CLAIMS:

41. (New) An integrated circuit device comprising:
a semiconductor die comprising synchronous-link dynamic random access memory circuitry;
a heat sink thermally coupled with to the semiconductor die; and
a housing encapsulating at least a portion of the heat sink.

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